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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
JP920000229US1

In Re Application Of: **Fujio et al**

MAR 17 2004

PATENT & TRADEMARK OFFICE
U.S. C. A. 2

Serial No.	Filing Date	Examiner	Group Art Unit
09/682,131	July 25, 2001	Chris C. Chu	2815

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH EMI PREVENTION STRUCTURE

Address to:

Assistant Commissioner for Patents
 Washington, D.C. 20231

37 CFR 1.97(b)

- The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

- The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

the statement specified in 37 CFR 1.97(e);

OR

the fee set forth in 37 CFR 1.17(p).

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Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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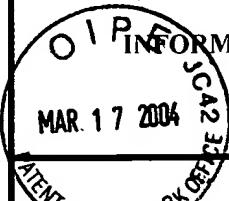
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INFORMATION DISCLOSURE CITATION
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MAR. 17 2004

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Docket Number (Optional)

JP920000229US1

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09/682,131

Applicant(s)

Fujio et al

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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	PUPA05-014015	1/22/1993	Japan			✓	
	PUPA2000-114723	4/21/2000	Japan			✓	
	PUPA2000-100983	4/7/2000	Japan			✓	

OTHER DOCUMENTS (*Including Author, Title, Date, Pertinent Pages, Etc.*)

Page 1 of 1

EXAMINER

DATE CONSIDERED

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